DATABASE

Here, courtesy of Oric Products International, we publish the first instalment of the 6502 programmers' reference card

MNEMONIC	DESCRIPTION	R1 R2	IMPLIED	IMMED	ZERO PAGE	Z-PAGE X	Z-PAGE Y	INDIRECT X	INDIRECT Y	RELATIVE ADDRESS	ABSOLUTE ADDRESS	ABSOLUTE X	ABSOLUTE Y	INDIRECT	P-REGISTER N	V	*	B	D	I	Z	C
ADC	ADD WITH CARRY	A		69	65	75		61	71		6D	7D	79		×	×					×	×
AND	LOGICAL AND	A		29	25	35		21	31		2D	3D	39		×						×	
ASL	ARITHMETIC SHIFT LEFT	A/M	0A(A)		06	16					0E	1E			×						×	×
BCC	BRANCH ON CARRY CLEAR									90												
BCS	BRANCH ON CARRY SET									B0												
BEQ	BRANCH IF EQUAL TO ZERO									F0												
BIT	COMPARE BITS WITH ACCUMULATOR				24						2C				M7	M6					×	
BMI	BRANCH ON MINUS									30												
BNE	BRANCH ON NOT EQUAL TO ZERO									D0												
BPL	BRANCH ON PLUS									10												
BRK	BREAK	S	00															×				
BVC	BRANCH ON OVERFLOW CLEAR									50												
BVS	BRANCH ON OVERFLOW SET									70												
CLC	CLEAR CARRY		18																			0
CLD	CLEAR DECIMAL		D8																0			
CLI	CLEAR INTERRUPT MASK		58																	0		
CLV	CLEAR OVERFLOW FLAG		B8													0						
CMP	COMPARE TO ACCUMULATOR			C9	C5	D5		C1	D1		CD	DD	D9		×						×	×
CPX	COMPARE TO REG-X			E0	E4						EC				×						×	×
CPY	COMPARE TO REG-Y			C0	C4						CC				×						×	×
DEC	DECREMENT MEMORY				C6	D6					CE	DE			×						×	
DEX	DECREMENT REG-X	X	CA												×						×	
DEY	DECREMENT REG-Y	Y	88												×						×	
EOR	EXCLUSIVE OR ACCUMULATOR	A		49	45	55		41	51		4D	5D	59		×						×	
INC	INCREMENT MEMORY				E6	F6					EE	FE			×						×	
INX	INCREMENT REG-X	X	E8												×						×	
INY	INCREMENT REG-Y	Y	C8												×						×	
JMP	JUMP TO ADDRESS	PC									4C			6C								