

# DATABASE

Here, courtesy of Oric Products International, we publish the 6502 programmers' reference card

MNEMONIC	DESCRIPTION	R1	R2	IMPLIED	IMMED	ZERO PAGE	Z-PAGE X
JSR	JUMP TO SUBROUTINE	PC/S					
LDA	LOAD ACCUMULATOR	A			A9	A5	B5
LDX	LOAD REG-X	X			A2	A6	
LDY	LOAD REG-Y	Y			A0	A4	B4
LSR	LOGICAL SHIFT RIGHT	A/M			4A(A)	46	56
NOP	NO OPERATION				EA		
ORA	INCLUSIVE OR ACCUMULATOR	A			09	05	15
PHA	PUSH A ONTO STACK	S A			48		
PHP	PUSH P-REGISTER ONTO STACK	S P			08		
PLA	PULL ACCUMULATOR FROM STACK	A			68		
PLP	PULL P-REGISTER FROM STACK	P			28		
ROL	ROTATE LEFT ONE BIT	A/M			2A(A)	26	36
ROR	ROTATE RIGHT ONE BIT	A/M			6A(A)	66	76
RTI	RETURN FROM INTERRUPT	P/PC/S			40		
RTS	RETURN FROM SUBROUTINE	PC/S			60		
SBC	SUBTRACT WITH CARRY	A			E9	E5	F5
SEC	SET CARRY	P			38		
SED	SET DECIMAL	P			F8		
SEI	SET INTERRUPT MASK (DISABLE)	P			78		
STA	STORE ACCUMULATOR	M A				85	95
STX	STORE REG-X	M X				86	
STY	STORE REG-Y	M Y				84	94
TAX	TRANSFER A TO X	X A	AA				
TAY	TRANSFER A TO Y	Y A	A8				
TSX	TRANSFER S TO X	X S	BA				
TXA	TRANSFER X TO A	A X	8A				
TXS	TRANSFER X TO S	S X	9A				
TYA	TRANSFER Y TO A	A Y	98				

Z-PAGE Y	INDIRECT X	INDIRECT Y	RELATIVE ADDRESS	ABSOLUTE ADDRESS	ABSOLUTE X	ABSOLUTE Y	INDIRECT	P-REGISTER							
								N	V	*	B	D	I	Z	C
				20											
B6	A1	B1		AD	BD	B9		x						x	
				AE	BC	BE		x						x	
				AC	BC			x						x	
				4E	5E			0						x	x
	01	11		0D	1D	19		x						x	
									x	x	x	x	x	x	x
				2E	3E			x						x	x
				6E	7E			x						x	x
								x	x	x	x	x	x	x	x
	E1	F1		ED	FD	F9		x	x					x	x
															1
															1
	81	91		8D	9D	99									
96				8E											
				8C											
								x						x	
								x						x	
								x						x	